

ABSTRACT

A LSI layout method for the LSI design of standard cell type is provided, by which sufficient noise suppression of power supply noise as well as sufficient
5 stabilization of power supply can be realized. Power supply capacitor cells are provided as one of the standard cells in addition to logic gate cells provided as one of the standard cells. Next, the capacitance value of the power supply capacitor cells is determined so as to correspond to the drive load capacity value of the logic
10 gate cells, and the power supply capacitor cells are arranged in the vicinity of the respective logic gate cells.